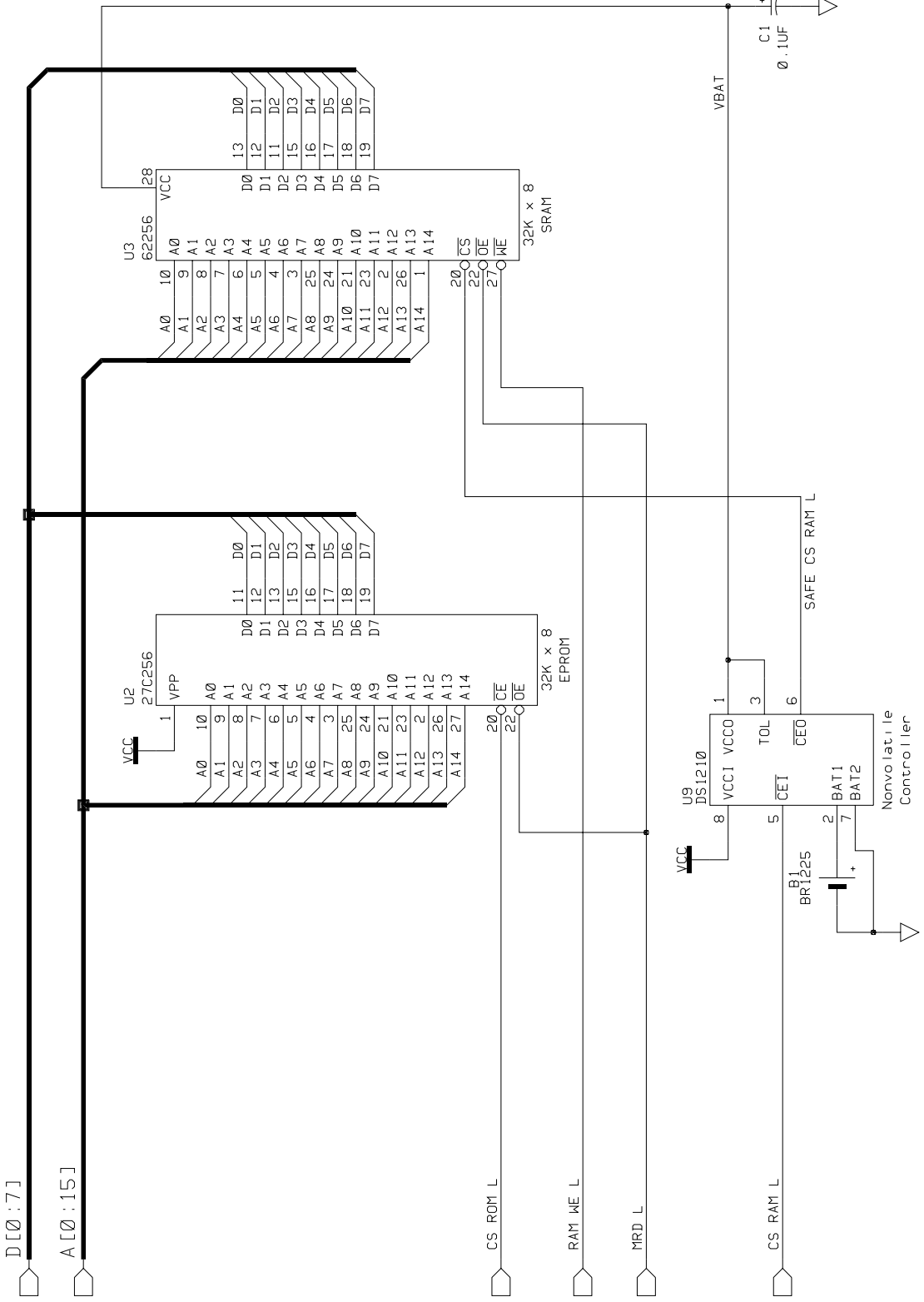


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 CPU
 19:35:40 27-Mar-05 CPU
 SHEET 1 OF 3

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 SIZE C

1 2 3 4 5 6 7 8



D C B A

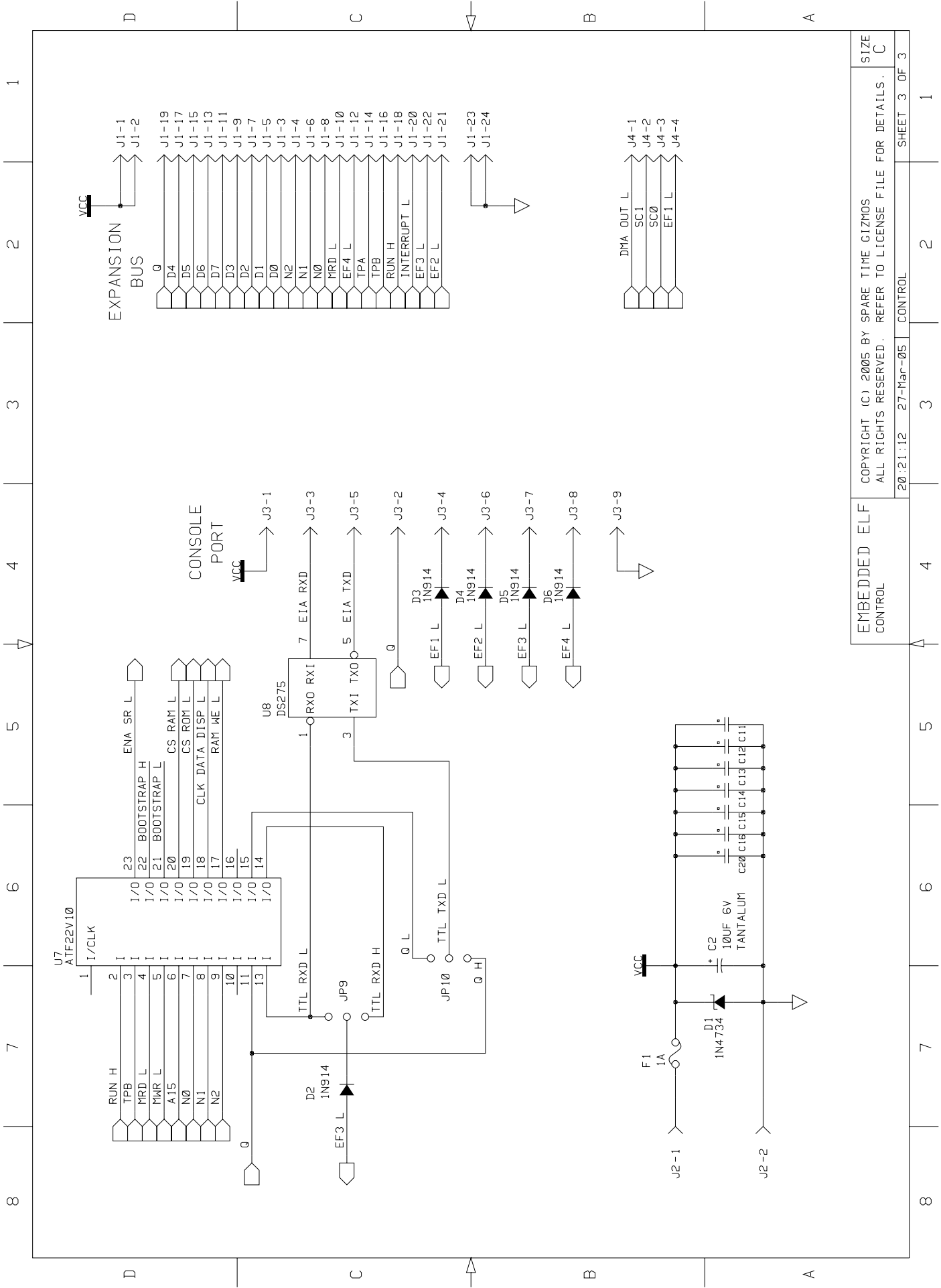
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EMBEDDED ELF
 MEMORY

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SIZE C
 SHEET 2 OF 3

1 2 3 4 5 6 7 8



EMBEDDED ELF CONTROL

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20:21:12 27-Mar-05 CONTROL SHEET 3 OF 3

SIZE C