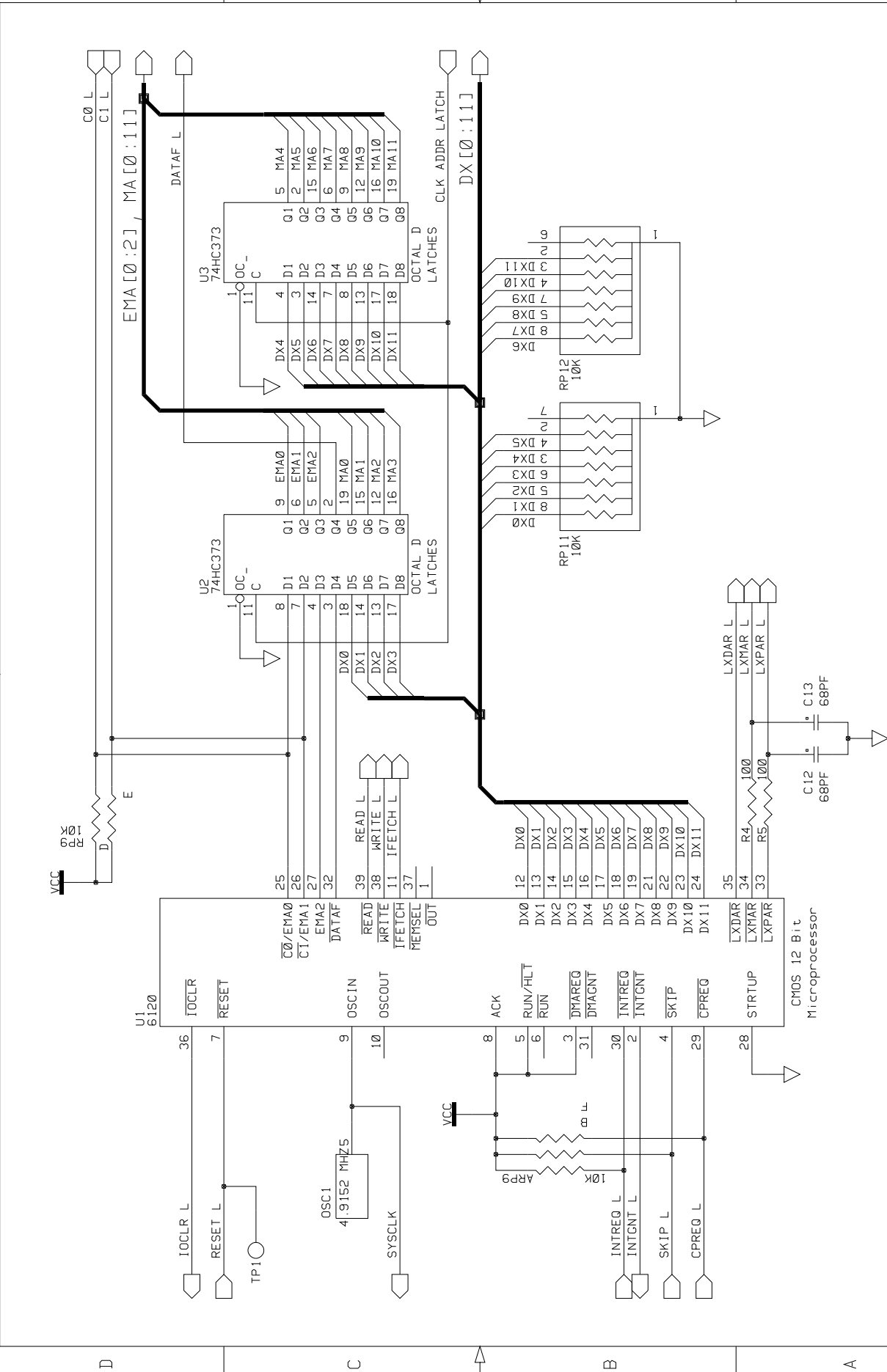


8 7 6 5 4 3 2 1

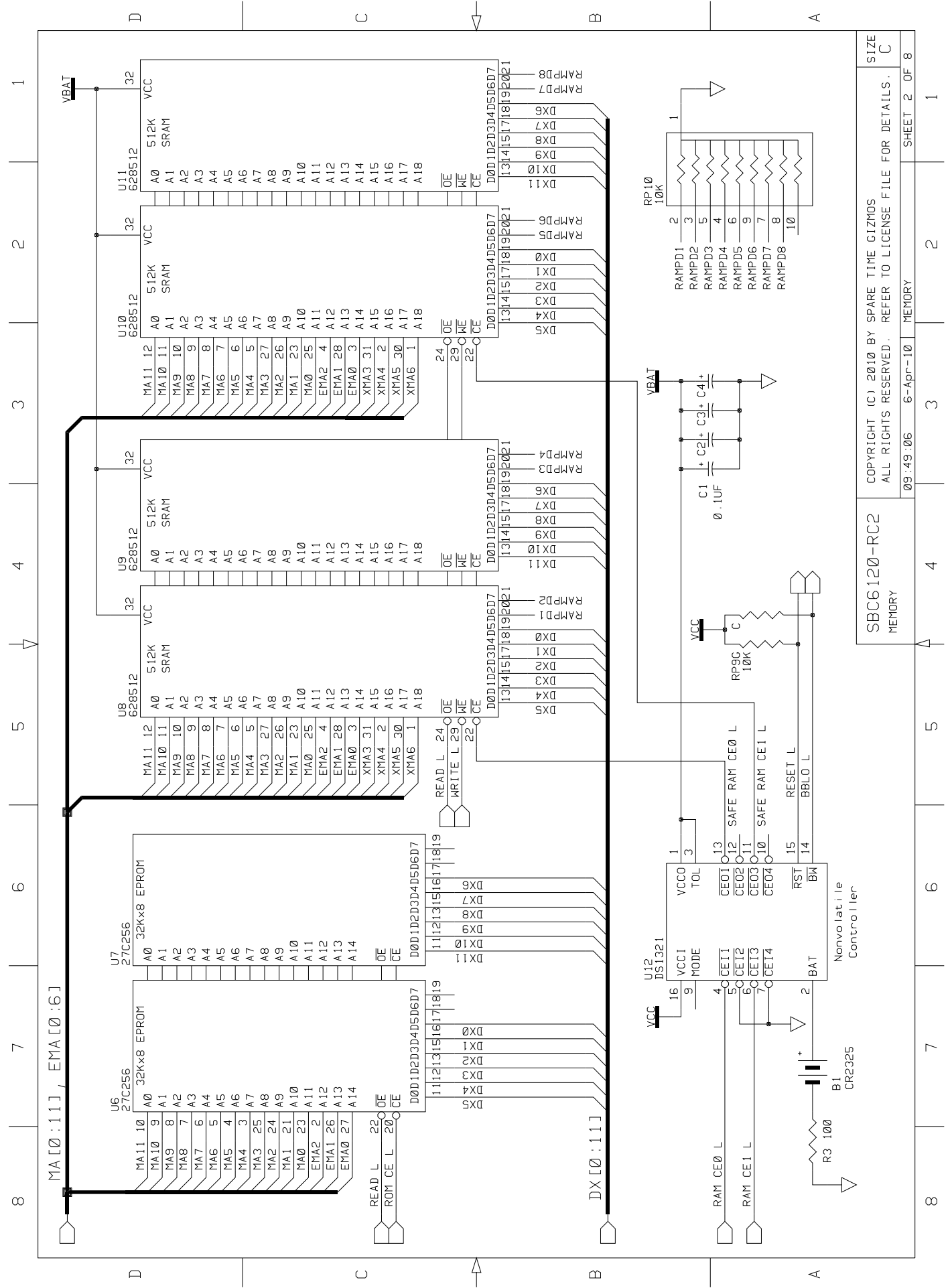


SBC6120-RC2
CPU

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09:48:11 6-Apr-10 CPU

SIZE C
SHEET 1 OF 8



SBC6120-RC2
MEMORY

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SIZE C
SHEET 2 OF 8

1 2 3 4 5 6 7 8

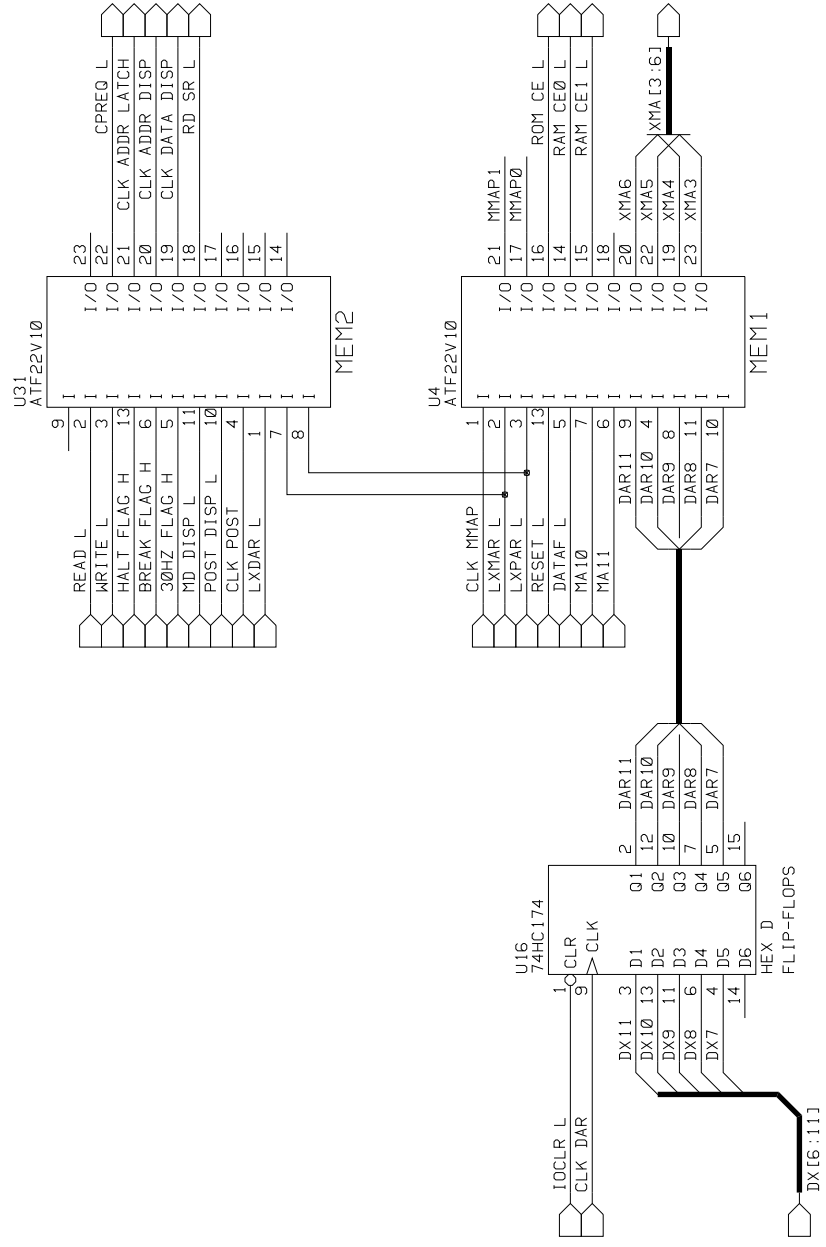
1 2 3 4 5 6 7 8

D C B A

D C B A

1 2 3 4 5 6 7 8

D C B A



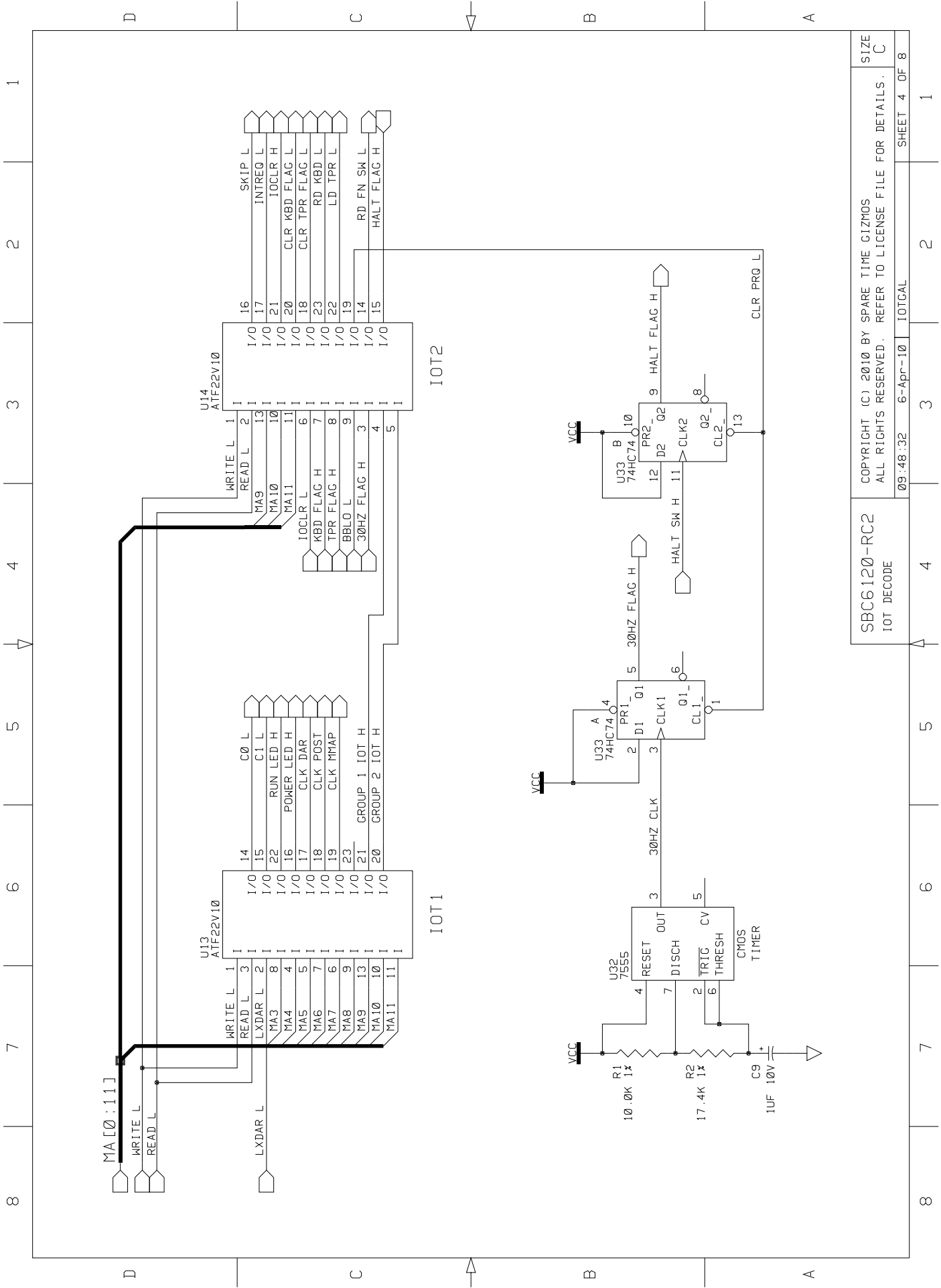
SBC6120-RC2
MEMORY DECODE

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09:48:55 6-Apr-10 MEMCAL SHEET 3 OF 8

1 2 3 4 5 6 7 8

D C B A

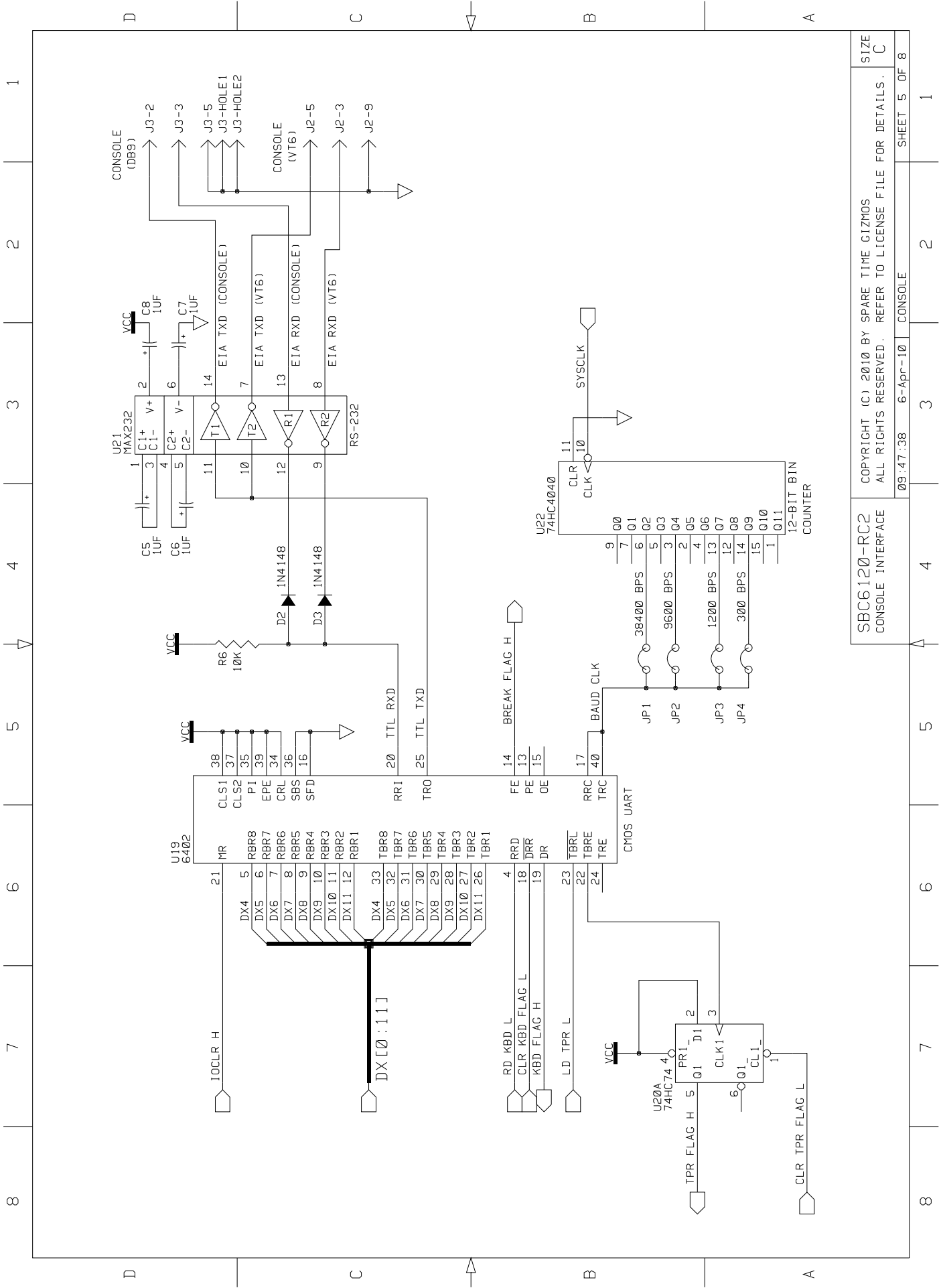


SBC6120-RC2
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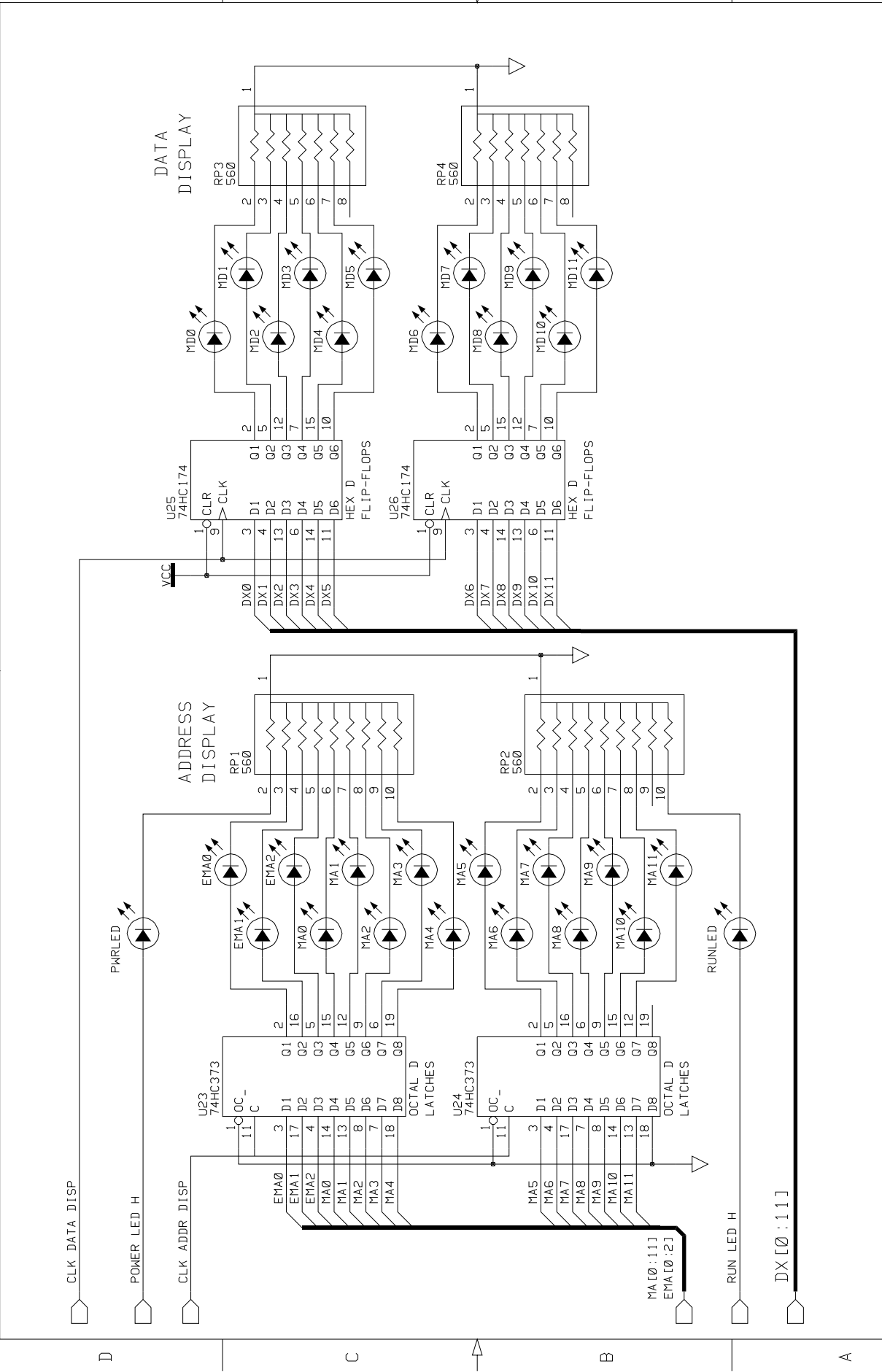
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09:48:32 6-Apr-10 IOTGAL

SHEET 4 OF 8

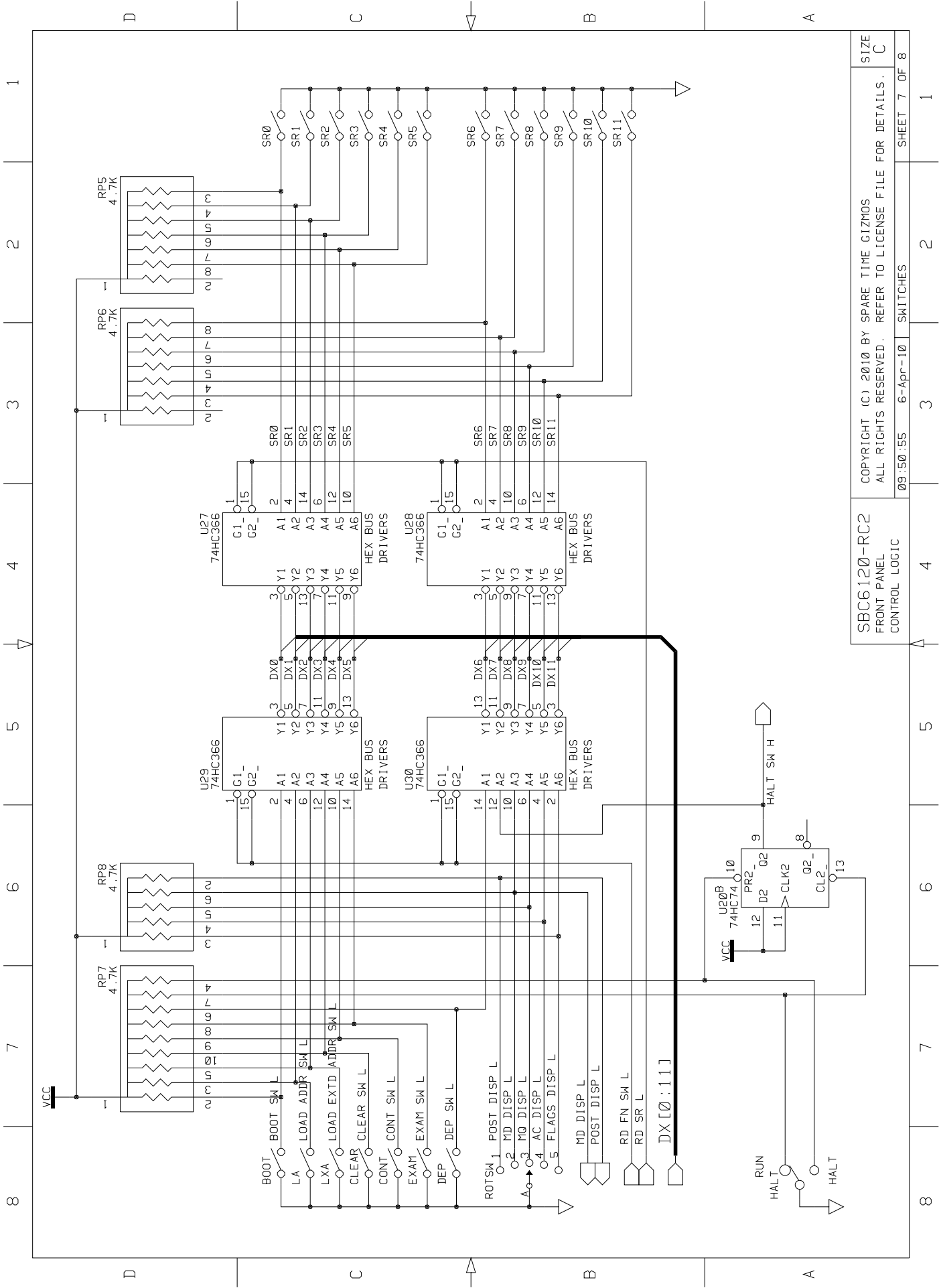


8 7 6 5 4 3 2 1



SBC6120-RC2
ADDRESS/DATA
DISPLAY

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SBC6120-RC2
 FRONT PANEL
 CONTROL LOGIC

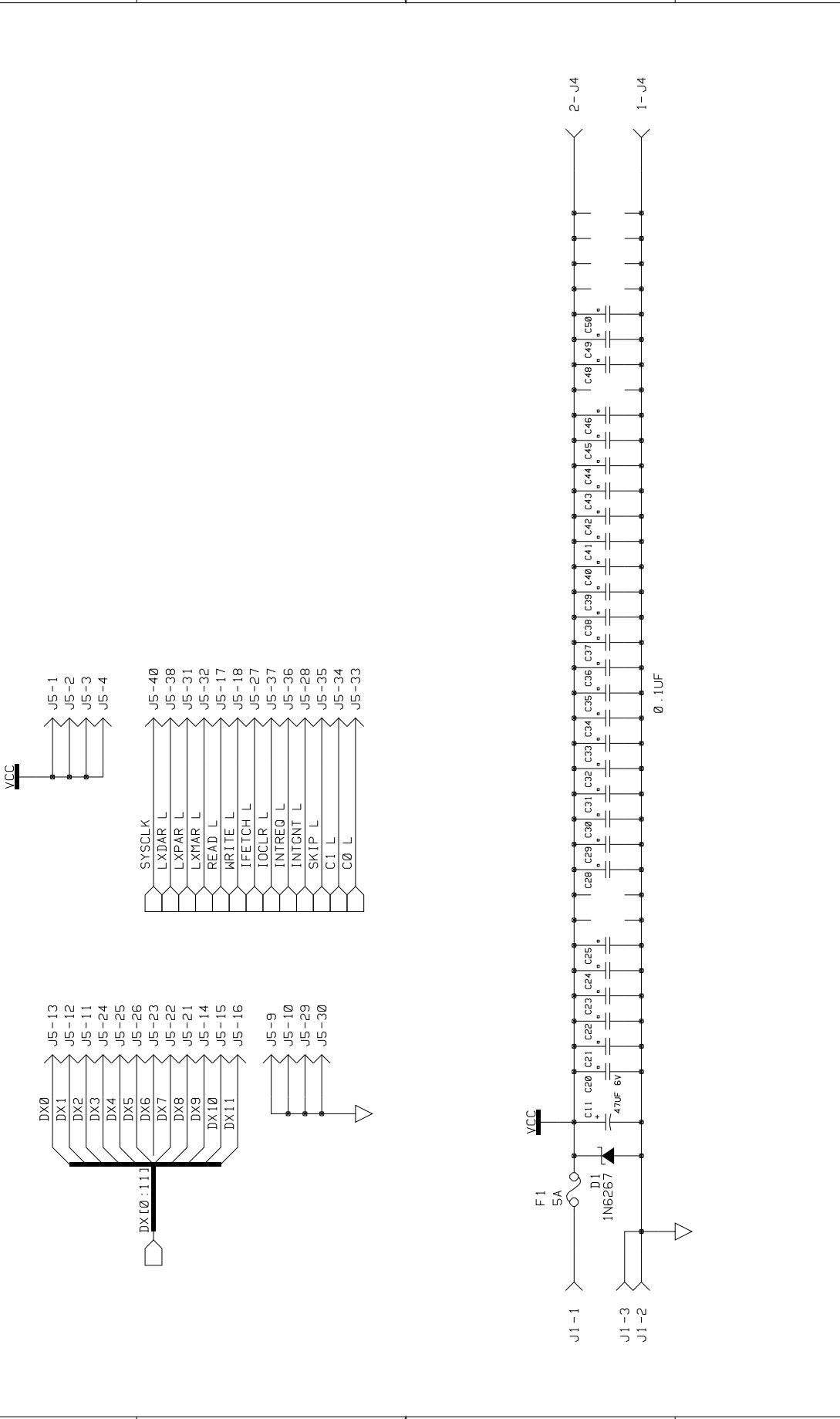
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SIZE
 C

09:50:55 6-Apr-10 SWITCHES SHEET 7 OF 8

8 7 6 5 4 3 2 1

D C B A



SBC6120-RC2			POWER		09:49:19 6-Apr-10		POWER		SHEET 8 OF 8	
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